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Electronic Version v1.1

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Title	of
Invent	ion

EXTERNAL VERIFICATION OF PACKAGE PROCESSED LINEWIDTHS AND SPACINGS IN SEMICONDUCTOR PACKAGES

Application Number:

Date:

First Named Applicant:

Thomas J. Fleischman

Confirmation Number:

Attorney Docket Number:

FIS920030167US1

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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files
us-ids	FIS920030167US1-usidst.xml
	us-ids.dtd
	us-ids.xsl
us-assignment	FIS920030167US1-usassn.xml
J	us-assignment.xsl
	us-assignment.dtd
	FIS9200230167assign1of2.tif
	FIS9200230167assign2of2.tif
us-request	FIS920030167US1-usrequ.xml
·	us-request.dtd
	us-request.xsl
us-fee-sheet	FIS920030167US1-usfees.xml
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	us-fee-sheet.dtd
us-declaration	FIS9200230167dec1of2.tif
application-body	fis920030167us1-trans.xml
	us-application-body.xsl
	application-body.dtd
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	mathml2.dtd
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	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
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	isodia.ent
	isolat1.ent
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	mmlextra.ent
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	soextblx.dtd
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	FIS92030167US1Drw5.tif
	FIS92030167US1Drw6.tif

us-declaration	FIS9200230167dec2of2.tif	
Comments		
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